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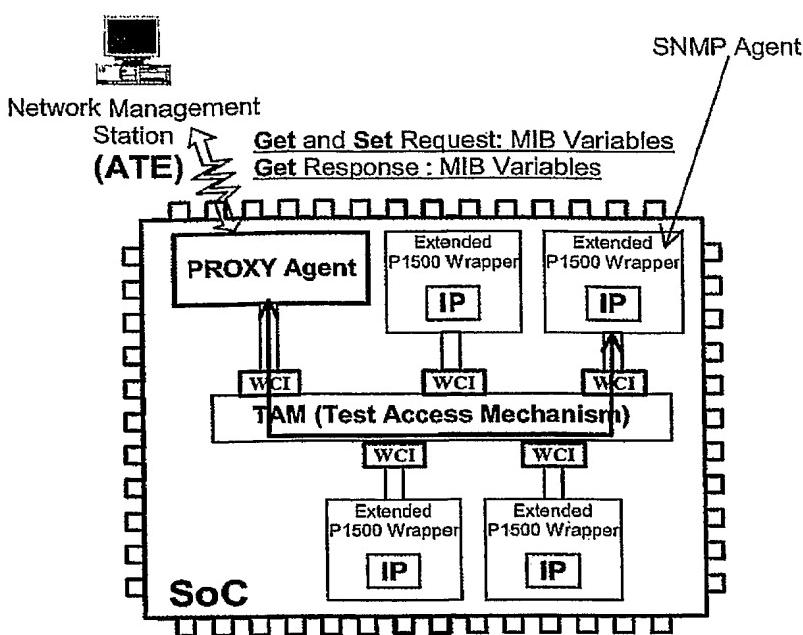
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(54) Title: INTEGRATED CIRCUIT CHIP WITH COMMUNICATION MEANS ENABLING REMOTE CONTROL OF TESTING MEANS OF IP CORES OF THE INTEGRATED CIRCUIT



(57) Abstract: The present invention relates to the testing of functional or IP cores forming part of a system on chip, SoC. The invention is implemented using a testing means and a communication means to test at least one functional or IP core. The testing means comprises a wrapper in which the core is embedded, this wrapper implements preferably the IEEE P1500 standard architecture but can also implement other standard architectures. The testing means can be extended to a Simple Network Management Protocol, SNMP, the widely adopted TCP/IP management protocol. The communication means comprises a test bus connected to the communication network. The proxy agent can implement the SNMP protocol, among others.



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